



GEYER KXO-V64 Features and Applications:

- LVDS Oscillator with supply voltage range +1.8 V ~ +3.6 V
- Package size: 3.2 x 2.5 x 1.0 mm, 6 pad footprint
- Frequency range: 10.0 ~ 312.5 MHz
- Wide temperature and low phase noise
- Moisture Sensitivity Level, MSL Level 1
- Recommended for Telecommunication, Network equipment, Industrial, etc.
- For further requirements, please contact us



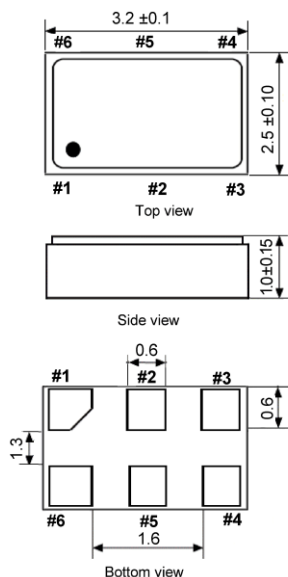
LVDS Clock Oscillator SMD-version

+1.8 / +2.5 / +3.3 V

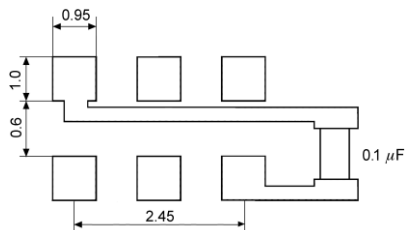
part no.	12.xxxxx
model	KXO-V64
frequency range	10.0 ~ 312.5 MHz
frequency stability incl. temperature stability, input voltage and load stability, aging.	$\pm 20, \pm 25, \pm 50$ ppm over $-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$ / $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$ (referred to $+25^{\circ}\text{C}$) $\pm 50, \pm 100$ ppm over $-55^{\circ}\text{C} \sim +125^{\circ}\text{C}$ (referred to $+25^{\circ}\text{C}$)
output load	100 Ohm & 5 pF LVDS
operating temperature	$-20^{\circ} \sim +70^{\circ}\text{C}$; $-40^{\circ} \sim +85^{\circ}\text{C}$ (=KXO-V64T) $-55^{\circ} \sim +125^{\circ}\text{C}$ (=KXO-V64F)
storage temperature	$-55^{\circ} \sim +125^{\circ}\text{C}$
supply voltage	+1.8 V DC $\pm 5\%$, +2.5 V DC $\pm 5\%$ or +3.3 V DC $\pm 5\%$
output voltage level	V_{OL} : 0.9 V min. / V_{OH} : 1.6 V max.
differential output voltage	0.247 V ~ 0.454 V, 0.350 V Typ
LVDS offset output voltage	1.125 V ~ 1.375 V, 1.250 V typ.
supply current max.	35 mA
start-up time max.	5 ms
symmetry	45 % / 55 % at $\frac{1}{2} V_{DD}$ level or 40 % / 60 % at $\frac{1}{2} V_{DD}$
rise time (Tr)/ fall time (Tf) max.	600 ps (Output level 20 % ~ 80 % of waveform)
disable delay/enable delay time max.	200 ns / 4 ms
Pin 1	Enable input voltage min.
Function	Disable input voltage max.
phase jitter (12 kHz ~ 20 MHz) typ.	55 fs at 156.25 MHz (3.3V)
phase noise typ.	-85 dBc / Hz at 10 Hz -114 dBc / Hz at 100 Hz -142 dBc / Hz at 1 kHz -152 dBc / Hz at 10 kHz -157 dBc / Hz at 100 kHz -160 dBc / Hz at 1 MHz
contents of reel	1000 pcs.



Dimensions (mm):

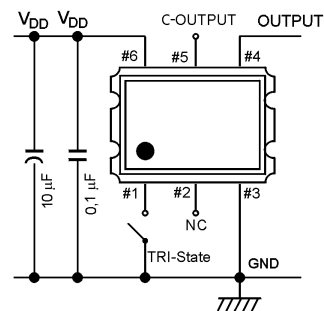


Suggested soldering pad:



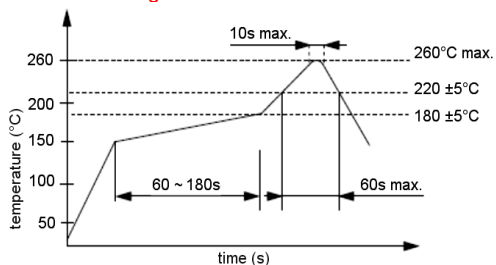
Note:
Capacitors of value 0,1 µF and 10 µF between V_{DD} and GND are recommended.

Test circuit:



PIN	Connection
1	Tri-state or NC
2	NC
3	GND
4	Output
5	C-Output
6	V_{DD}

Reflow soldering condition:



Tape specification:

