



Clock Oscillator

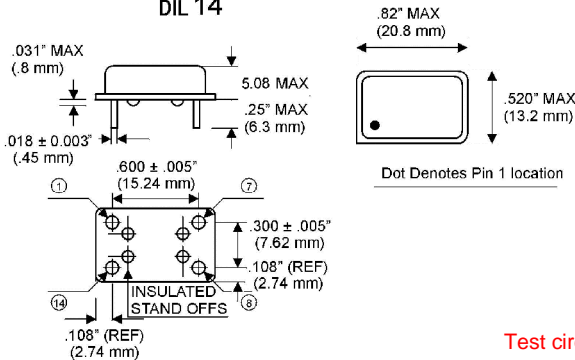
+5.0V

TTL / HCMOS, TRISTATE

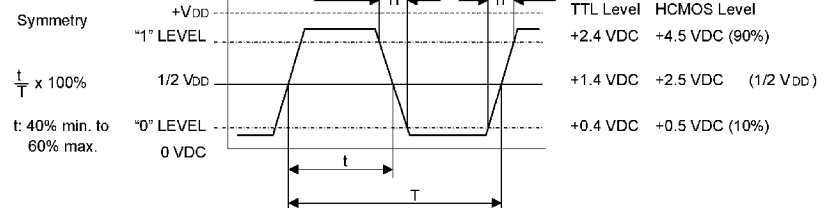
part no.	12.xxxxx
model	KXO-400
package	DIL 14
frequency range	0.5 ~ 100 MHz
frequency stability	standard ± 100 ppm available ± 25 ppm $\sim \pm 100$ ppm
operating temperature range	standard $-20^{\circ}\text{C} \sim +70^{\circ}\text{C}$ available $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$ (=KXO-405)
storage temperature range	$-55^{\circ}\text{C} \sim +125^{\circ}\text{C}$
input voltage	+5.0V DC $\pm 10\%$
input current max.	20mA 0.5 ~ 20MHz 40mA 20.1 ~ 70MHz 60mA 70.1 ~ 100MHz
symmetry	standard 60/40% at $\frac{1}{2}$ VDD available 55/45% at $\frac{1}{2}$ VDD
rise and fall time max.	10 ns 0.5 ~ 25 MHz 6 ns 25.1 ~ 70 MHz 4 ns 70.1 ~ 100 MHz
"0" level max.	+0.5V (10% VDD)
"1" level min.	+4.5V (90% VDD)
enable/disable delay time max.	100 ns
output load	1 – 8 TTL or CL = 50 pF typ.
logic family	TTL or HCMOS compatible
packing unit	25 pcs.

Dimensions (mm):

DIL 14

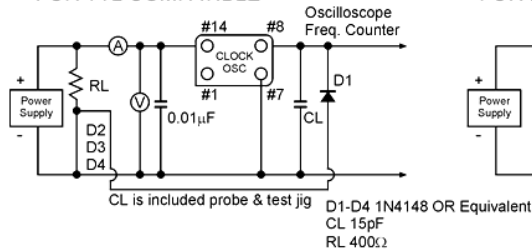


Waveform:

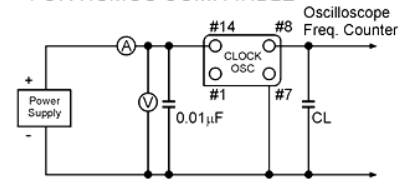


Test circuit:

FOR TTL COMPATIBLE



FOR HCMOS COMPATIBLE



PIN	CONNECTION
#1 (#1)	„L“(0V) „H“(+5V) or OPEN
#7 (#4)	GND
#8 (#5)	Z OUTPUT
#14 (#8)	VDD

Z: high impedance